

# Spinline - FPGA and $\mu$ Processor based platform

## Taken and sharing advantages of both worlds

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# Main objectives

**Spinline : the Rolls-Royce I&C Software based nuclear safety platform dedicated for category A functions**

**Overview of the software aspects**

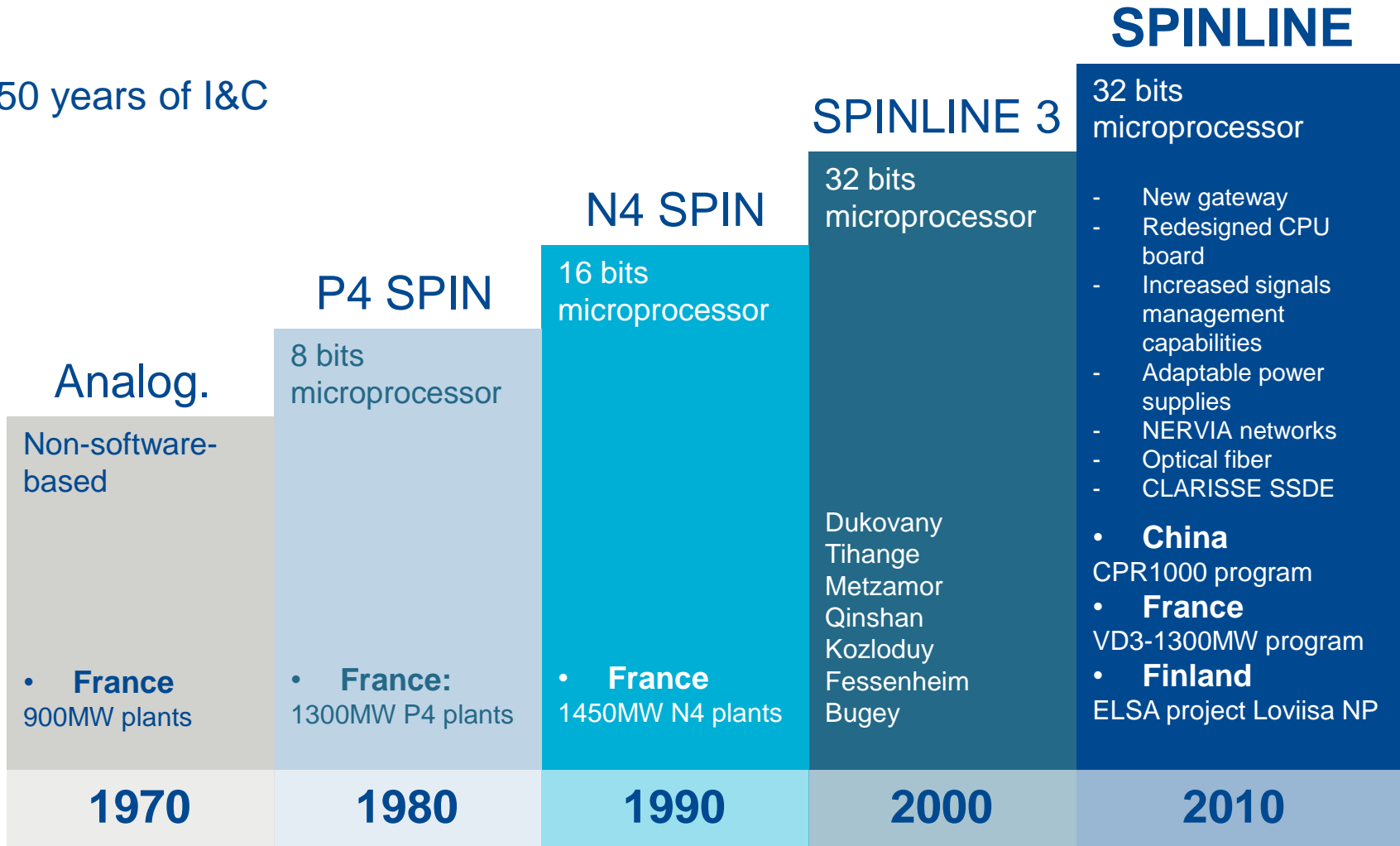
**Overview of embedded FPGAs aspects**

**Licensing of Spinline in different Regulatory frameworks**



# Spinline the 4<sup>th</sup> digital generation

50 years of I&C



# Spinline platform

## the Spinline components:

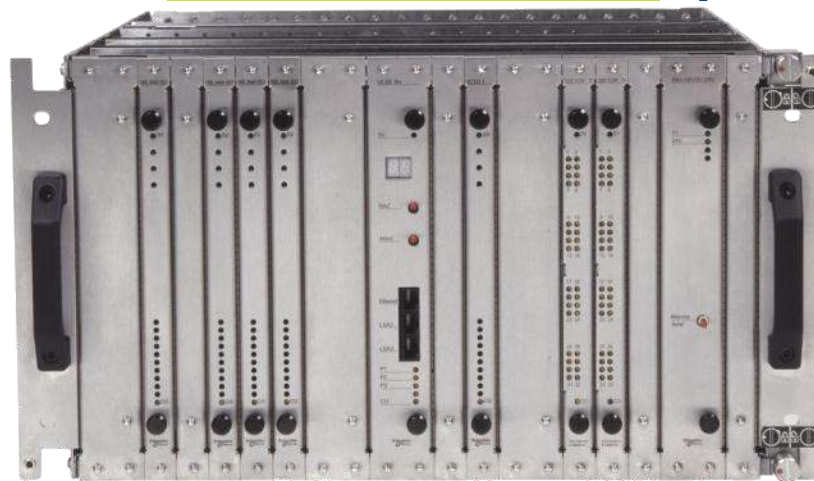
- The components of the generic platform software are:
  - The standardized, Class 1E configurable Operational System Software (OSS)
  - The Class 1E application-oriented library of reusable software components,
  - The Class 1E software embedded in “intelligent” boards
  - A non-Class 1E set of tools integrated in a System and Software Development Environment ((CLARISSE), used to design and configure the systems and equipment software.
- Spinline embedded FPGA for electronic functions
- Spinline hardware boards, modules, racks, cabinets



# Spinline processing rack

*Interface boards*

*Power source*



*Main boards:  
inputs/outputs*

*Main boards:  
inputs/outputs*

*UC25 N+ LSA*



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# Spinline software

## A Spinline software is composed of two parts:

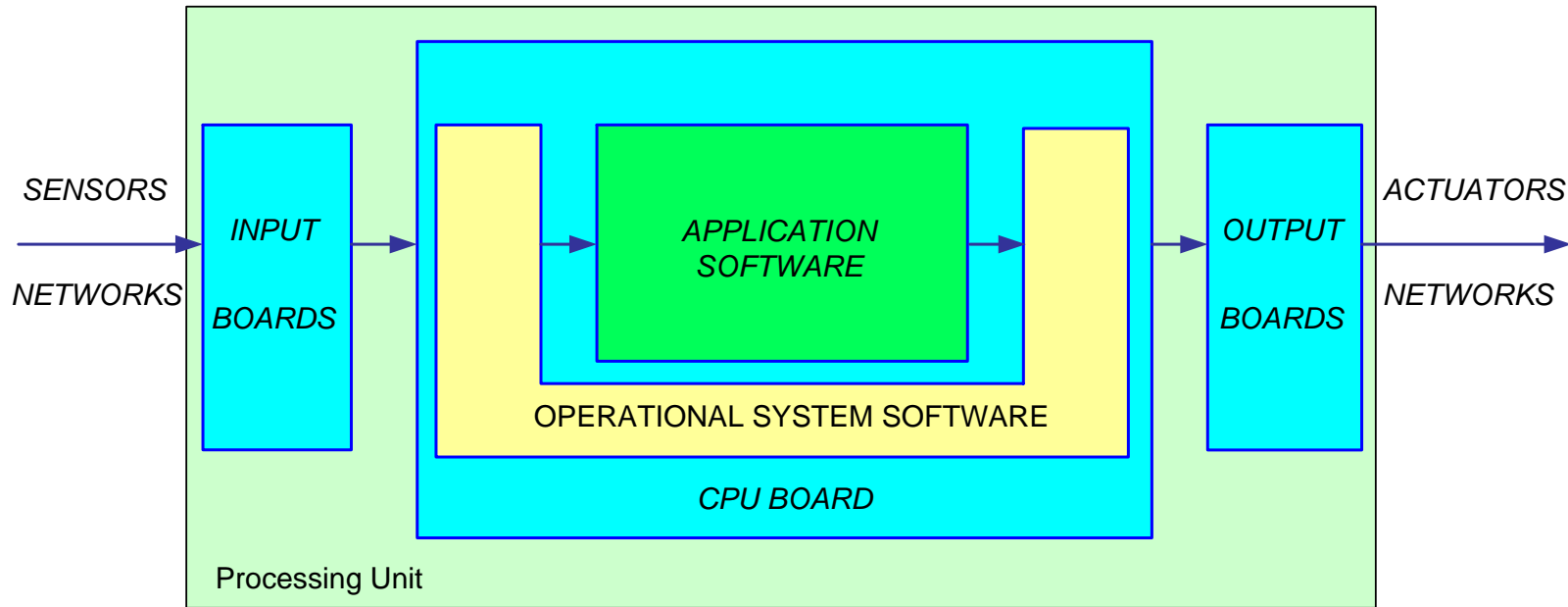
- The “Operational System Software” (OSS) is standard,
  - It has been developed according to IEC 60880 and comes as a software component to be used on the CPU boards of the processing units.
  - It is ready for use after a simple configuration to fit the needs of the customer I&C systems. It provides basic functions like communication, data acquisition or services to be used by the application software
- The “application software” is specific and must be developed by a dedicated team.
  - It implements the Equipment Functional diagrams (the plant specific application)



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# Spinline software

## The software Installed on a Processing Unit



# Spinline software

## Main characteristics :

- no use of interrupts
- no dynamic memory allocation
- no support for event driven multi-tasking
- Modular, structured and simple
- functions embedded restricted to the only need of the operation of the equipment and the I&C safety functions
- perform and manage self-tests
- include defensive programming where appropriate
- ensure safety oriented features (when a failure is detected)
  
- Entirely designed, verified and validated according to IEC 60880

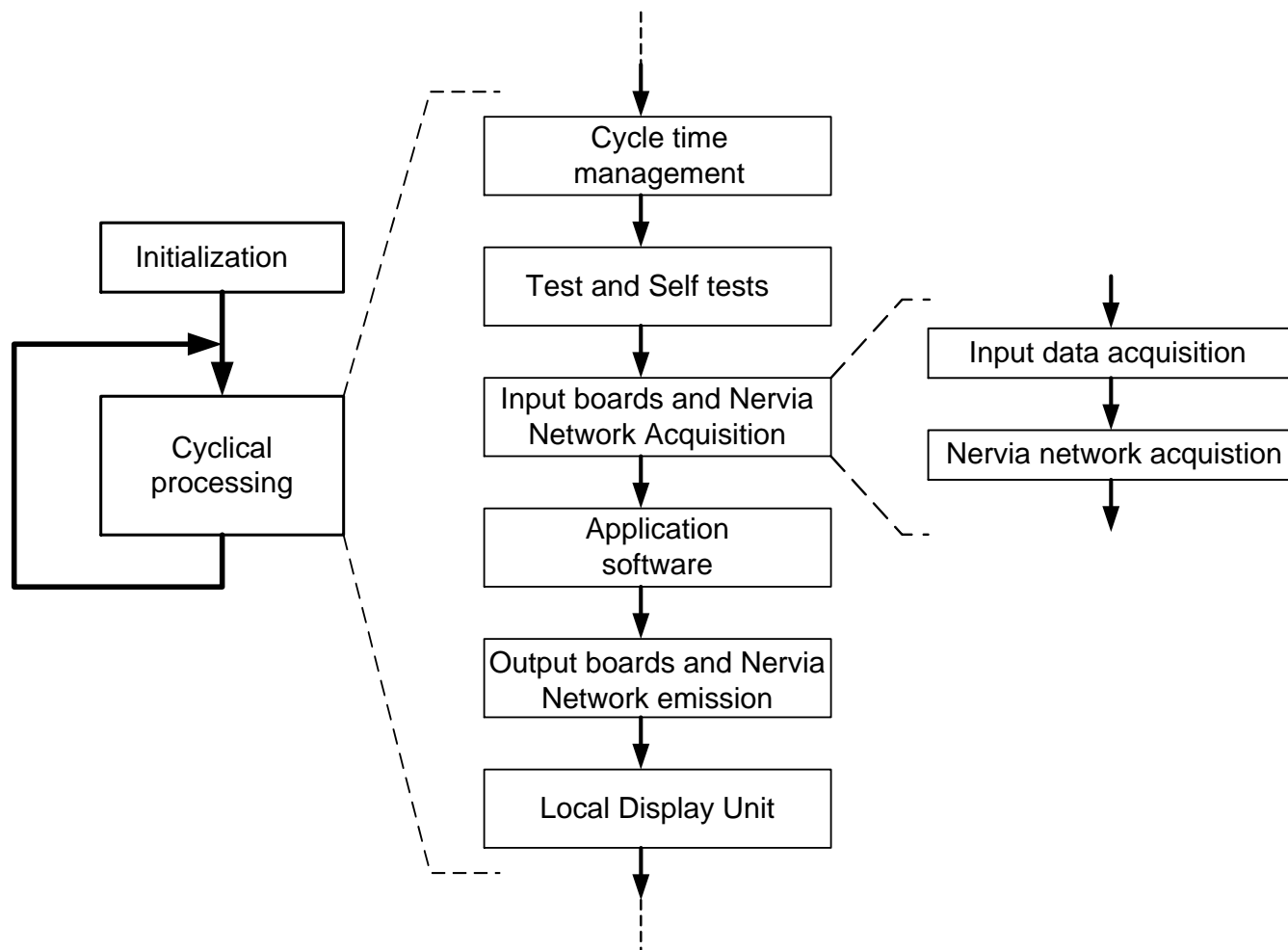


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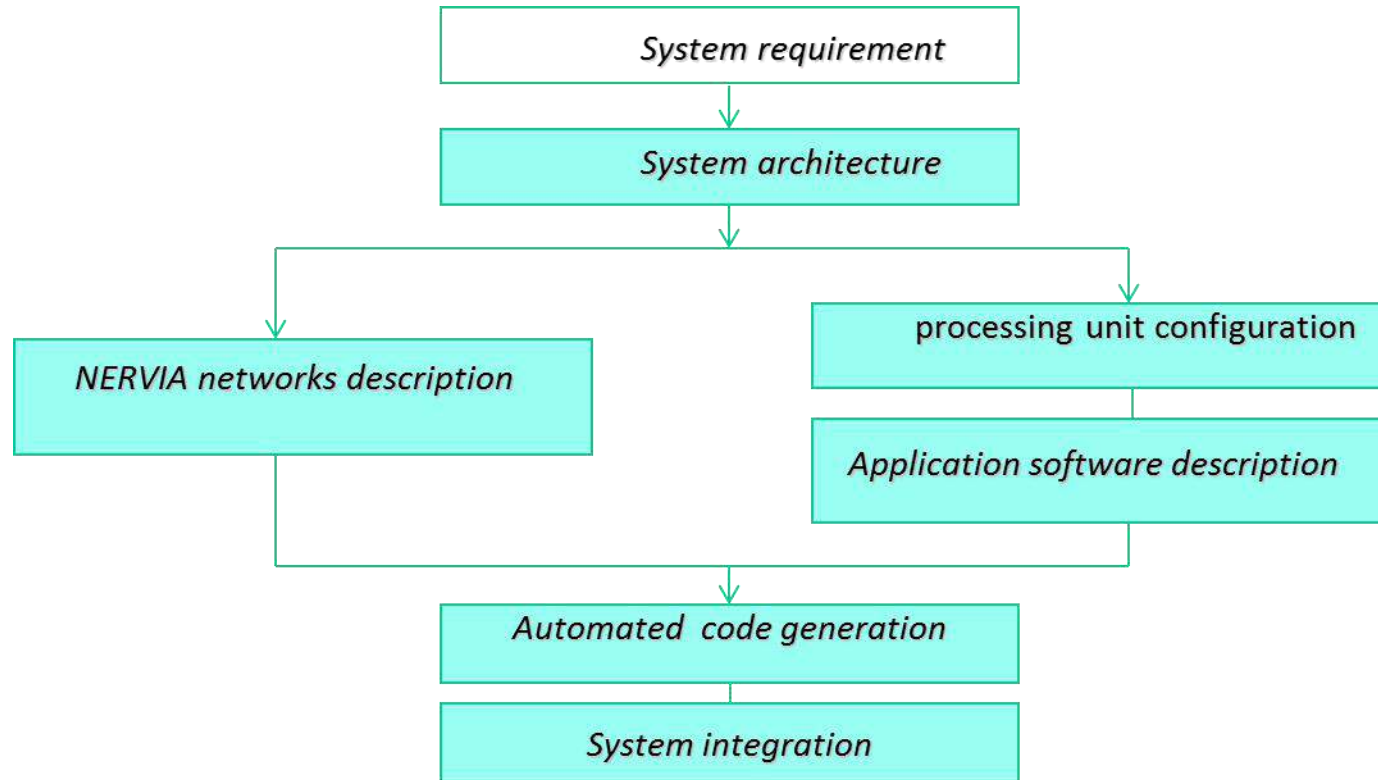
# Spinline software

Sequential, cyclic and fully deterministic execution of functions



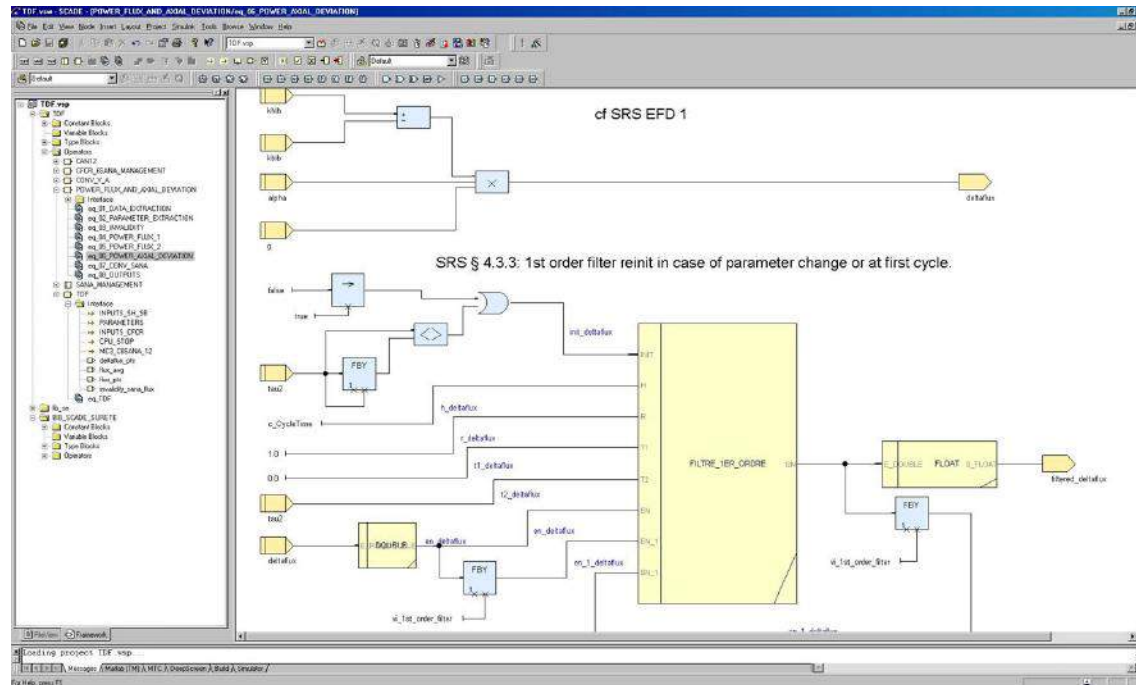
# Spinline software

## The SW development process with Clarisse SDDE



# Spinline software

## Plant specific application development with Scade

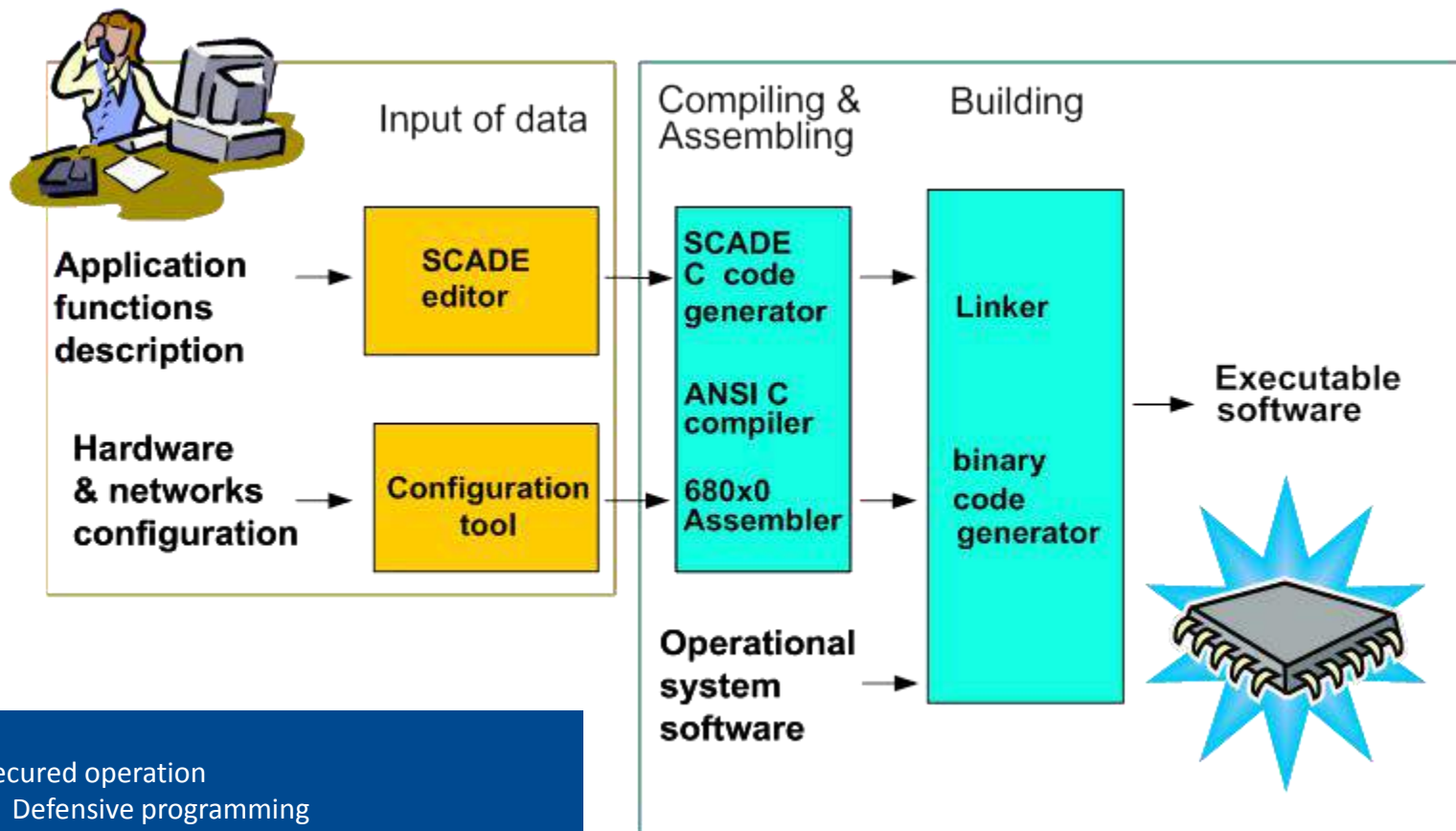


- controlled language : no loops, no interrupts, no dynamic memory allocation
- includes semantics checks : completeness, type consistency, initial data, deadlock
- used in critical systems (Airbus, Eurocopter, railways signalling)
- compliant with requirements of IEC60880



# Spinline software

## CLARISSE SDDE - Software binary production



### Secured operation

- Defensive programming
- Write-protected software
- Physical access to the CPU board is needed for modifications

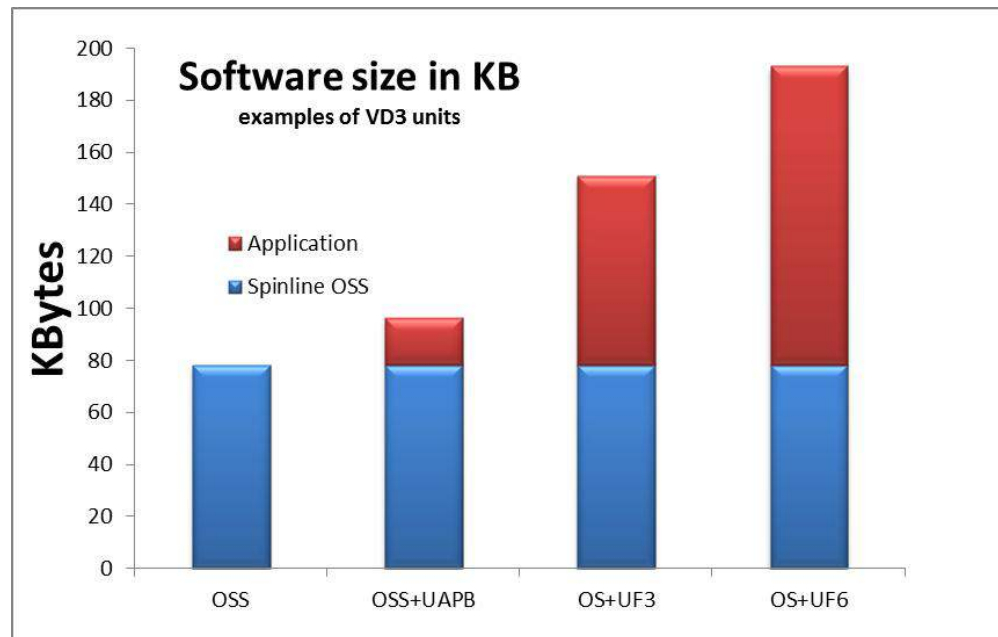
# Spinline software

## Final characteristics for assessments

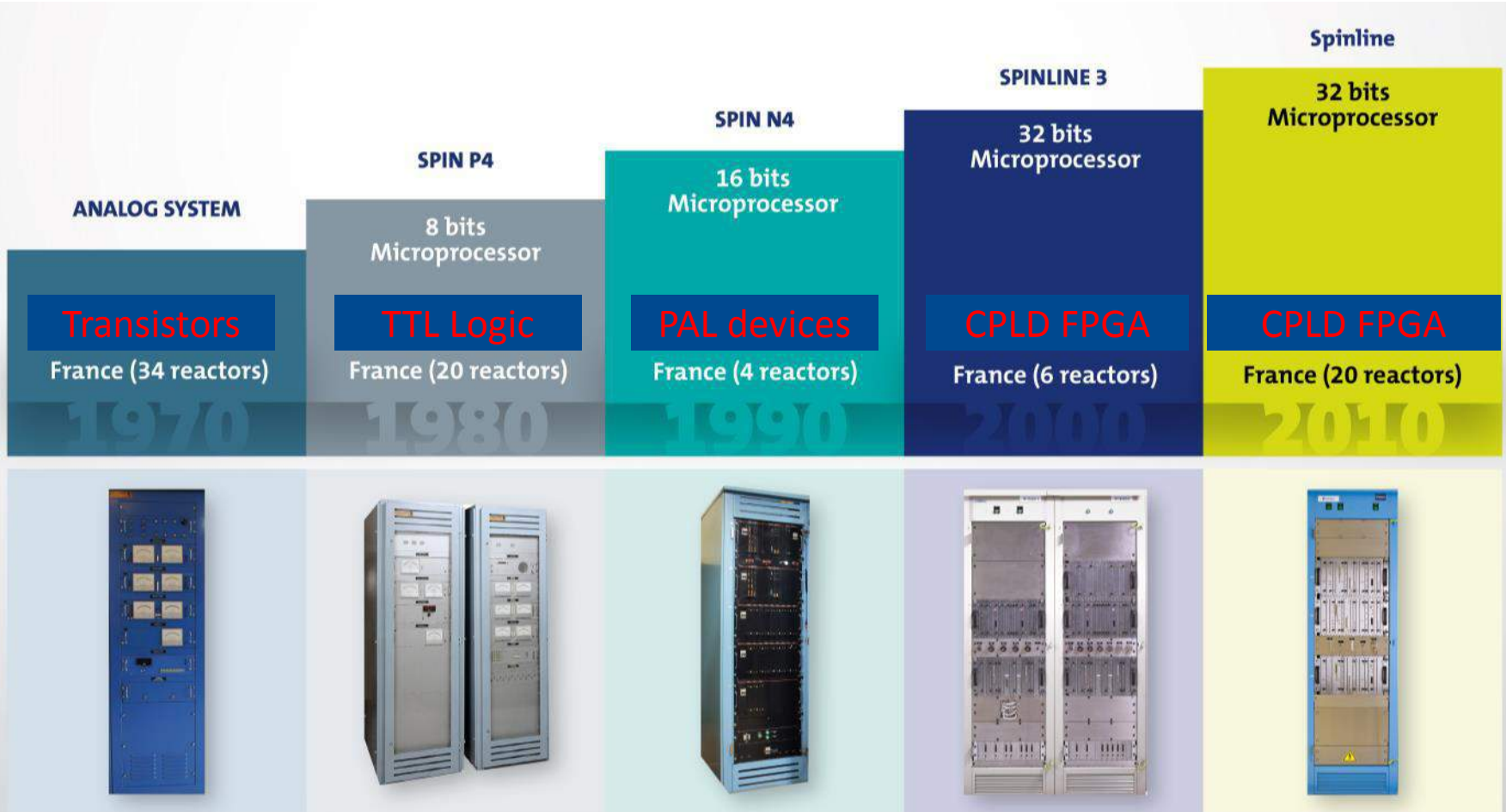
### Safety and Transparency

- All safety software components are available and reviewable
- C language are commonly known and tools exist for third parties evaluation
- Function block diagrams description are close to specifications and easy to ready
- No Safety software black boxes

### Size



# Historical use of FPGA at Rolls-Royce I&C



# FPGA at Rolls-Royce I&C

## **FPGAs used to provide hardware design solutions ranking from**

- simple electronic functions internal to hardware devices (in the context of dedicated hardware or programmed logic boards)
- implementation of complex application functions

## **In the design of electronic boards :**

- existing boards, boards foreseen to be refurbished and additional new boards.

## **Higher level of complexity used for specific non safety equipment**

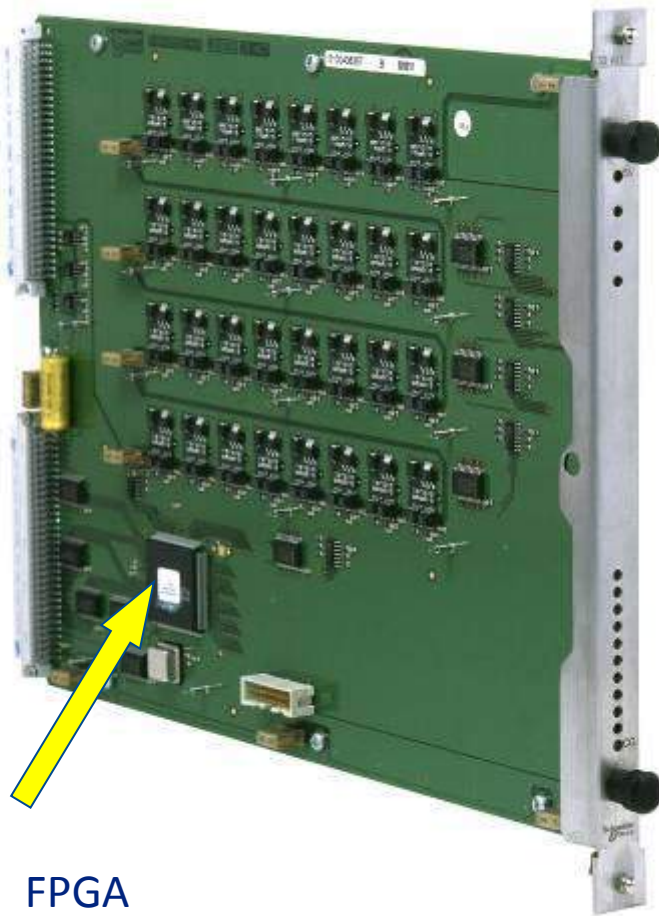
- ex Implementation of rod control system – cyclor (France 900MW)



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# Design of electronic boards for class 1 equipment

## 32 Actuator Board design



**Previous actuator board:**  
1 microprocessor + logic circuits;

**New actuator board (on the left):**

- Electronic function implemented in an Actel FPGA
- 30 000 gates
- Performs the following functions :
  - Bus interface
  - Processor interface (registers)
  - Surveillance of the outputs, using a short impulse test
  - Board self-tests
- All other components on the board are analog components



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# Design of electronic boards for class 1 equipment

## 16 analog inputs board



FPGA

**Previous analog board: 1 microprocessor + logic circuits; 6 analog channels per board**

### **New analog board:**

- The electronic function is implemented into an Actel FPGA
- 40 000 gates
- Performs the following functions :
  - Interface with the bus
  - Interface with the processor (dual ported ram)
  - Control of acquisition (16 inputs in 1 ms)
  - For each input, processing of the input value for gain and offset adjustment, performed by an Arithmetical and Logic Unit
  - Board self-tests
- All other components on the board are analog components



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# Design of electronic boards for class 1 equipment

## FPGA Design Flow

- Successfully used for all electronic functions design
- Compliant with IEC62566
- Entirely and exclusively applied in the Hardware electronic department
- **Applied to more than 15 class1 boards**

Board Design Process :

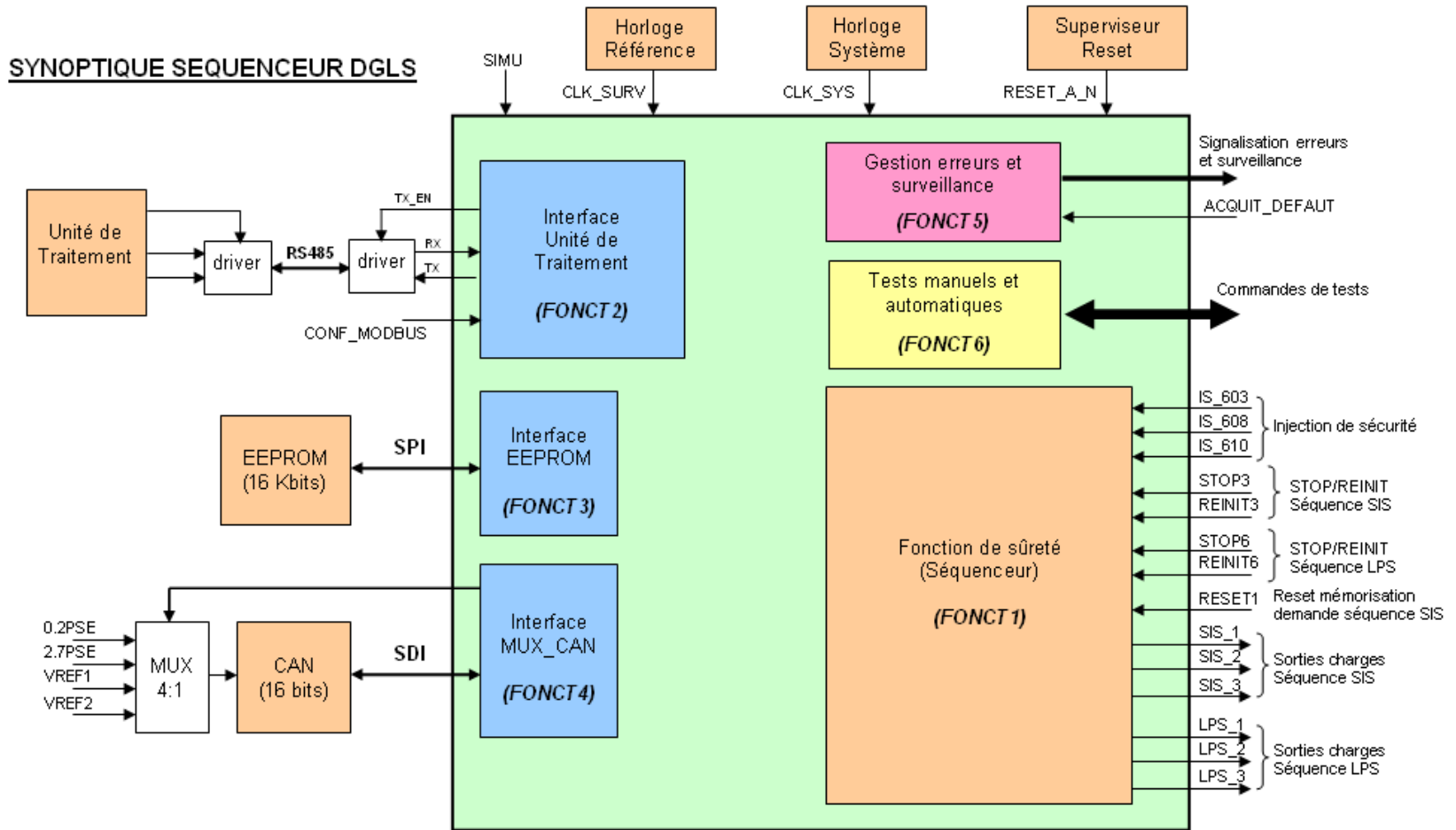
- simulation ,
- prototypes tests (PROTO1, PROTO2, ...)



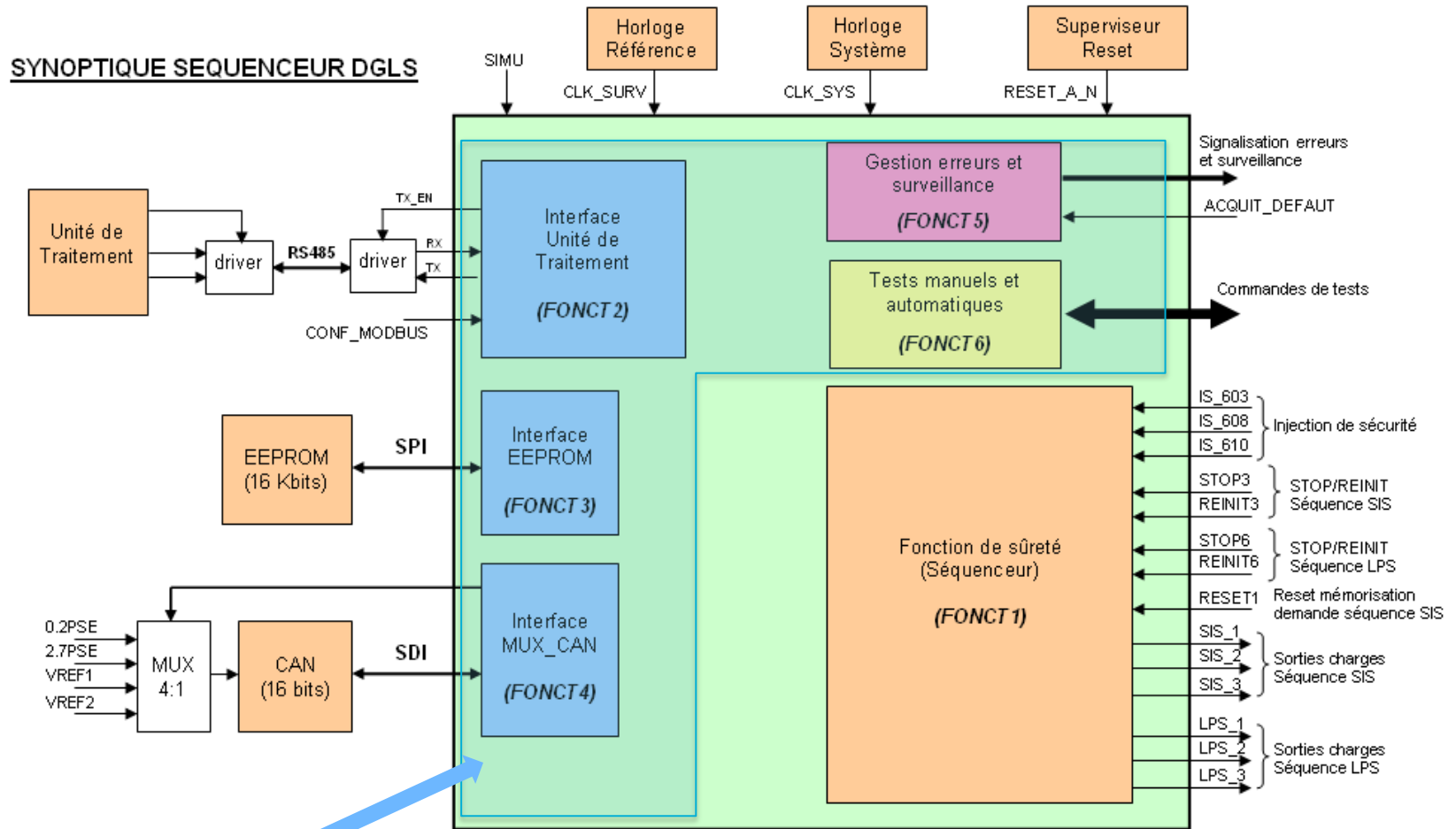
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# FPGA Thoughts about a Class 1 application (R&D)

## Top level specification of a Diesel Generator Load Sequencer



## Top level specification of a Diesel Generator Load Sequencer



Equivalent to an Operational System Software



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# FPGA thoughts about the use of the technology

- **Clear advantages for electronic functions**
  - Flexibility and ease for implementing hardware functions
  - Reduction of discrete components, higher board reliability
  - Replacement of sensible components (adjustment pot)
- **Need for skills attention**
  - Design and test activities require dedicated skills with electronic hardware background rather than System or software background
  - Coding HDL differs significantly from software coding
  - Architecture of the FPGA and targeted design within the FPGA must be well understood and drives the design beyond the functions to implement
- **Need for technical attention**
  - No ease for floating point format calculation, developing an FPU is needed,
  - no mathematic functions, no algebraic, no Elementary transcendental functions
  - Embedded self tests, Definition of safe states,
  - Partitioning, separation, local specific redundancy, parallel propagation
  - Design and testing tool behavior
  - Parameter handling (conditions for updates)



# About licensing of Spinline

## 3 cases:

- UNITED STATES, US NRC:
  - Spinline : safety evaluation of the Spinline generic platform,
- FRANCE, ASN/IRSN:
  - EDF - VD3 1300MW : Plant specific project  
20 NPP refurbishment currently in deployment phase,
- FINLAND, STUK,
  - FORTUM - ELSA project : Plant specific project  
refurbishment of I&C systems at the Loviisa NPP in progress



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# Spinline generic assessment - US NRC (1)

## NRC RAIs LTR and documentation reviews :

The NRC staff submitted 3 sets of Requests for Additional Information (RAIs) representing 67 questions/comments covering:

- Process and procedures :
- Technical clarification
- All topics explained, understood and accepted by the NRC

## NRC Factory audit:

- NRC one week audit with IRSN France as an invited party to share the audit content and outcomes
- aimed to more communications about procedures, complex technical information and access to low-level details.

US NRC audit report :

“The audit team lead indicated that all audit objectives were satisfactorily met.  
No open items or concerns were identified.”



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# Spinline generic assessment - US NRC (2)

## the US NRC evaluation in a nutshell:

- In 2009, Rolls-Royce submitted the initial LTR for the acceptance evaluation
- In 2010, the evaluation project was launched by the US NRC
- The NRC staff submitted 3 sets of RAIs filled-in by Rolls-Royce
- The NRC performed hardware qualification audit and factory audit
  
- **In September 2014, the NRC finalized the SER**

US/NRC : “No generic open items or unusual plant-specific open items are needed to be addressed by an applicant or licensee referencing the topical report”



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# VD3 1300 MW project : EDF- IRSN/ASN (1)

## Specific licensing aspects for the software

- EDF fully independent software testing
  - all final VD3 1300 MW software source code
  - quality assessment of the code
  - robustness assessment of the code
  - 3 different departments of EDF involved
  
- IRSN fully independent additional software testing
  - Selected final software testing
  - dedicated purposes for IRSN
  
- **no critical issue are identified**



## VD3 1300 MW project : EDF- IRSN/ASN (2)

### the VD3 1300 MW project in a nutshell:

- Rolls-Royce started the project at end of 2010
- The overall licensing in 2 parts:
  - the generic Spinline platform
  - plant specific project
- 180 documents licensing related,

**This project is currently the world largest I&C modernization program**

- **At the end of 2014, the final assessment of IRSN has been officially published :**

**IRSN : “The Spinline platform is fully suitable for the development of class 1 equipment and for category A functions.”**



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## “Independent Type approval” of the Spline platform:

- The type approval process aims to get certificates from a third party independent from the supplier
- Accreditation ISO 17020, ISO 17025 or ISO 17065
- Rolls-Royce contracted the activity with the German company “ISTec TUV Rheinland”
- The assessment covered in detail more than 40 documents completed by more than 150 referenced documents
- It has started in mid 2014 and has been finalized for all existing parts in September 2016
- **4 certificates : Hardware, Software, software tools, FPGA**
- **Completed by the Type approval report**



ISTec-TUV : “The report confirms that the Spline and the hardwired platform conform to their specifications, and that the design and manufacturing processes are adequate to ensure high quality I&C products for implementation in systems important to safety in nuclear power plants.”



# Conclusion

- **Both worlds, FPGA based or Software based, have their specific advantages and may be used to implement targeted functions,**
- **Both can be used efficiently and consistently for their most relevant field.**
- **A platform initially and entirely designed for nuclear I&C systems remains certainly a significant factor for safety demonstration.**
- **Spinline, including both technologies, has shown to be fully licensable within different international Regulatory frameworks**
- **The highest level of confidence stands certainly on the easiness:**
  - **to provide the safety evidences of the solutions**
  - **to reach the best common understanding among the stakeholders.**



Thank you for your attention!

Comments? Questions?

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October, 2016



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